

Fig. 1 (a)

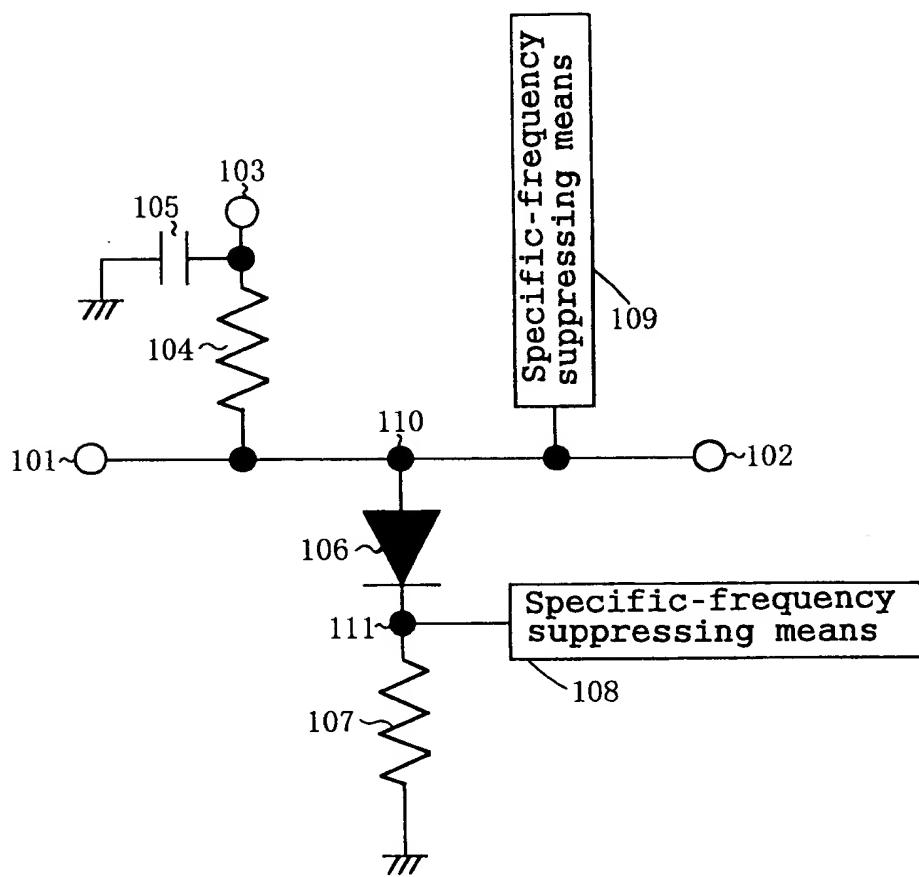


Fig. 1 (b)

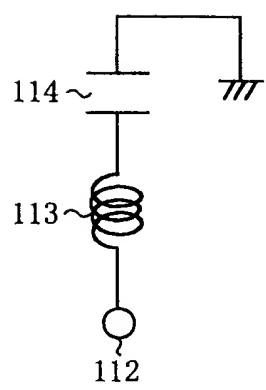


Fig. 1 (c)

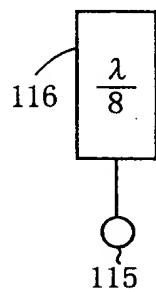


Fig. 1 (d)

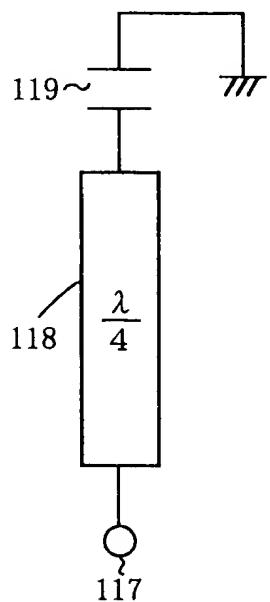


Fig. 2 (a)

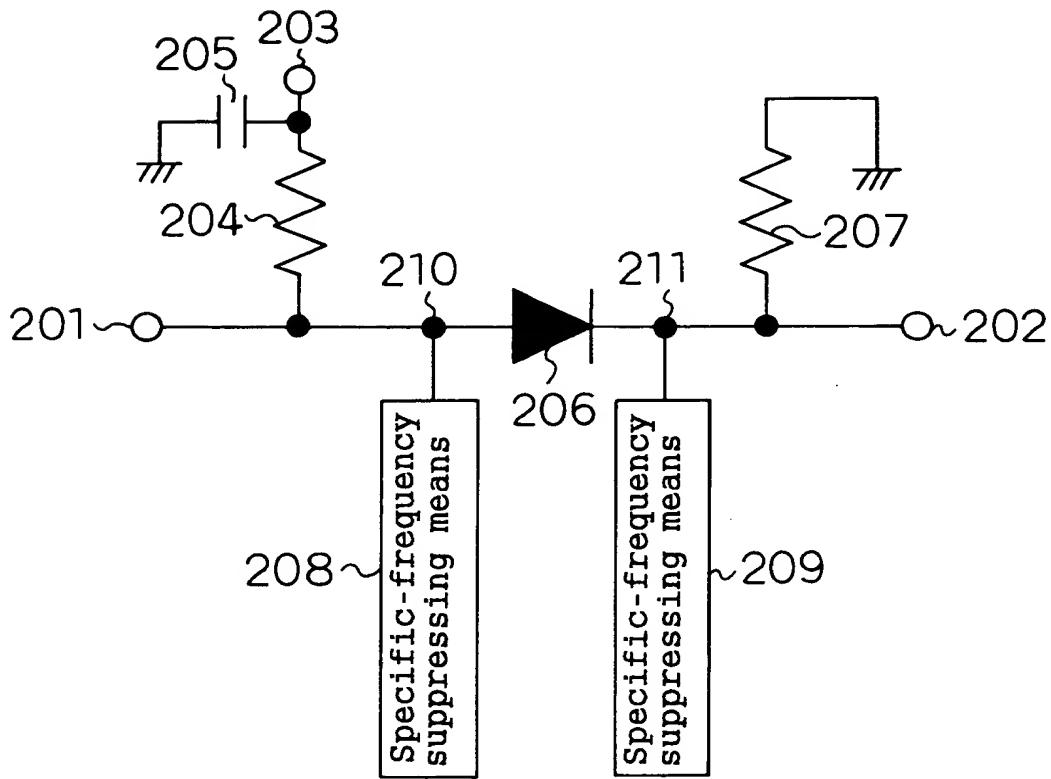


Fig. 2 (b)

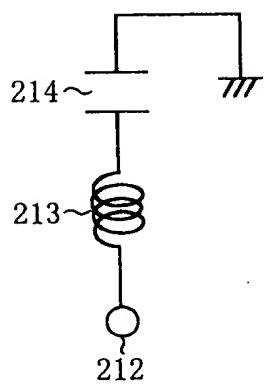


Fig. 2 (c)

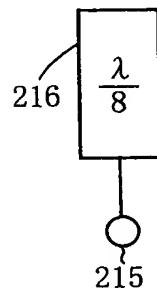


Fig. 2 (d)

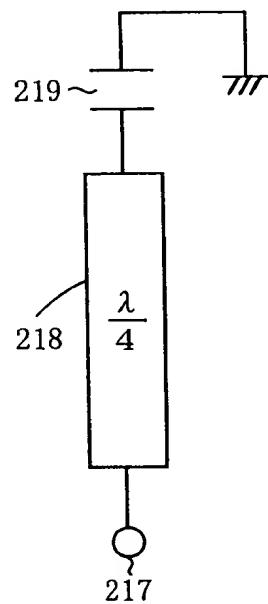


Fig. 3 (a)

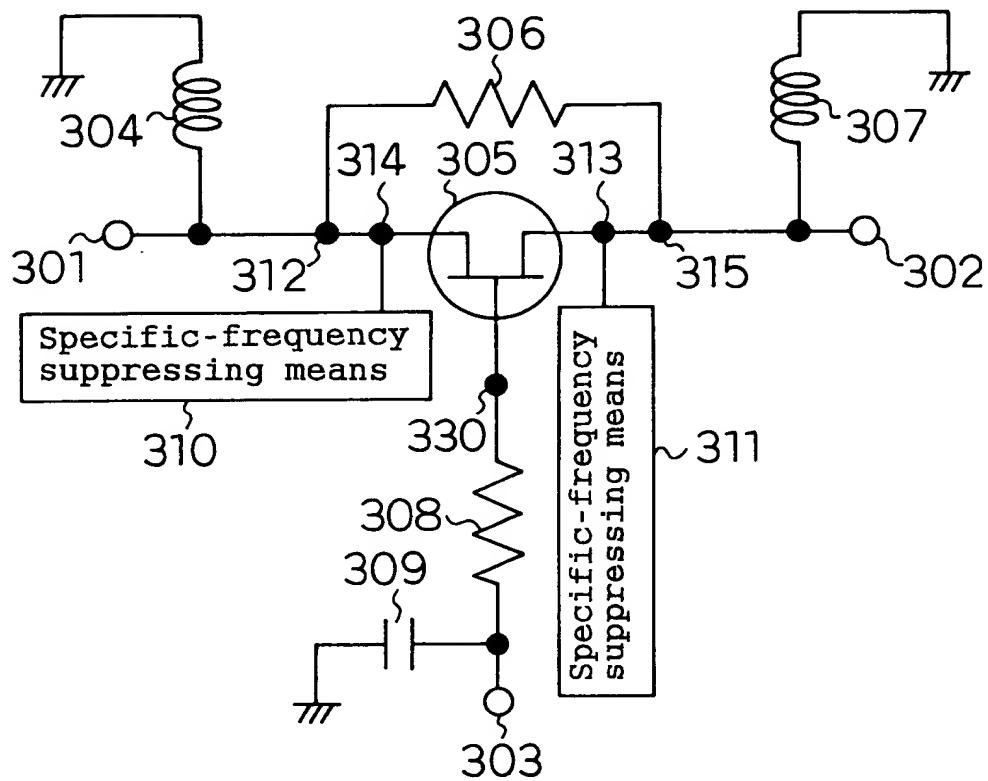


Fig. 3 (b)

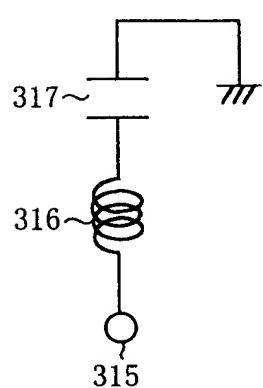


Fig. 3 (c)

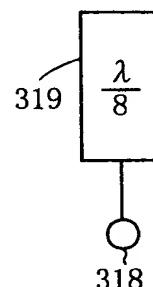


Fig. 3 (d)

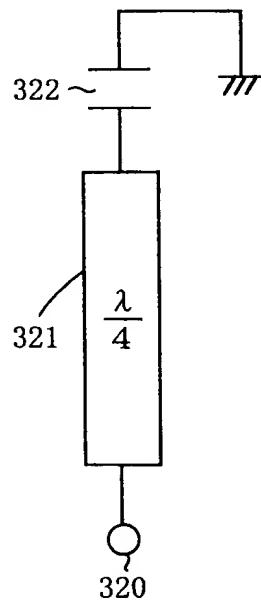


Fig. 4

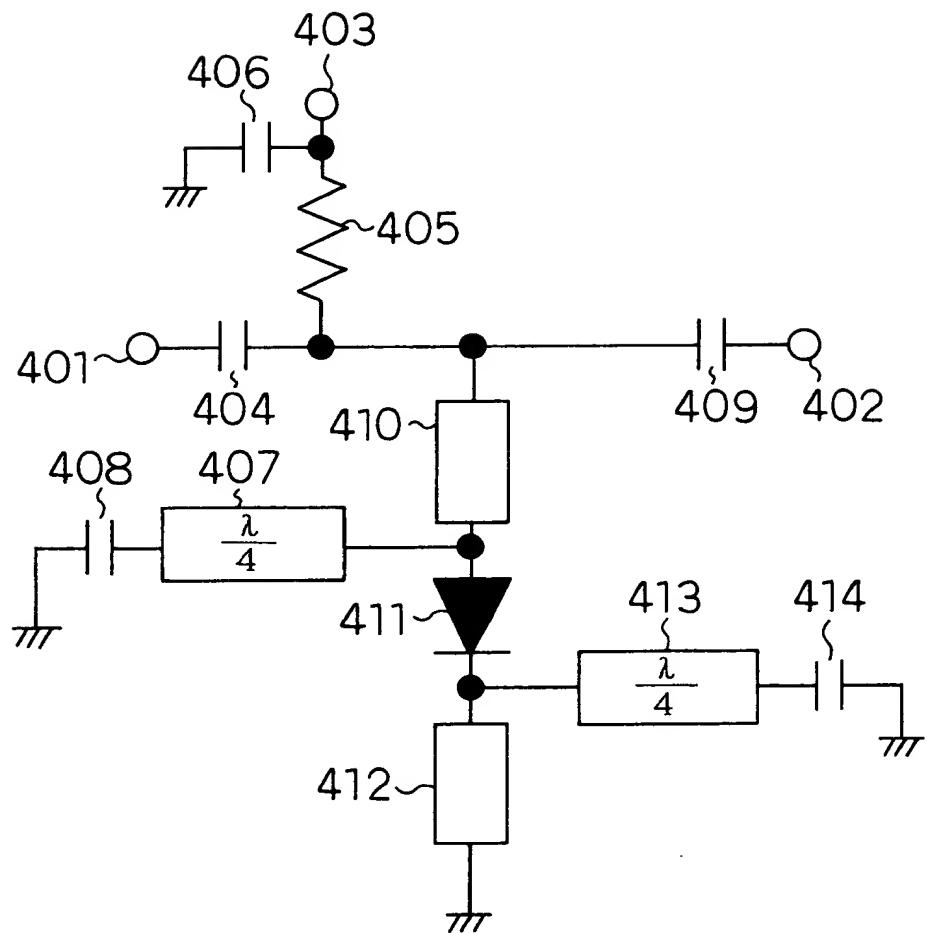


Fig. 5 (a)

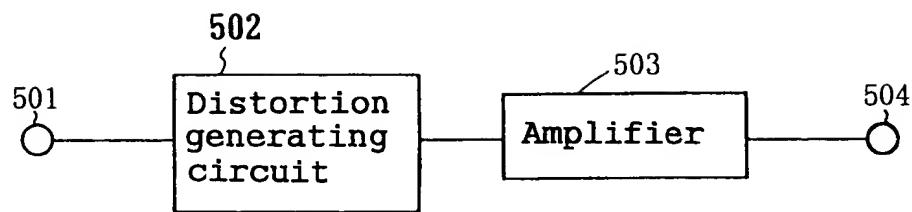


Fig. 5 (b)

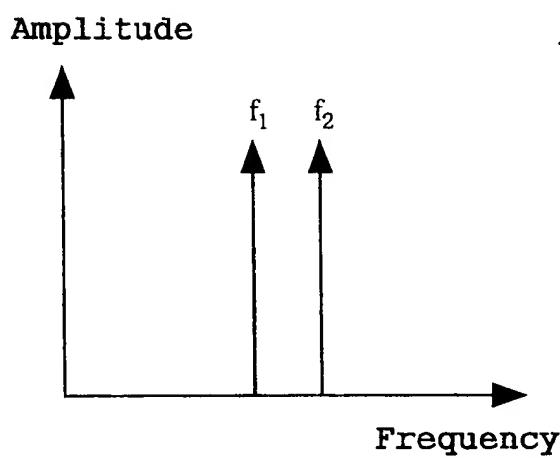


Fig. 5 (c)

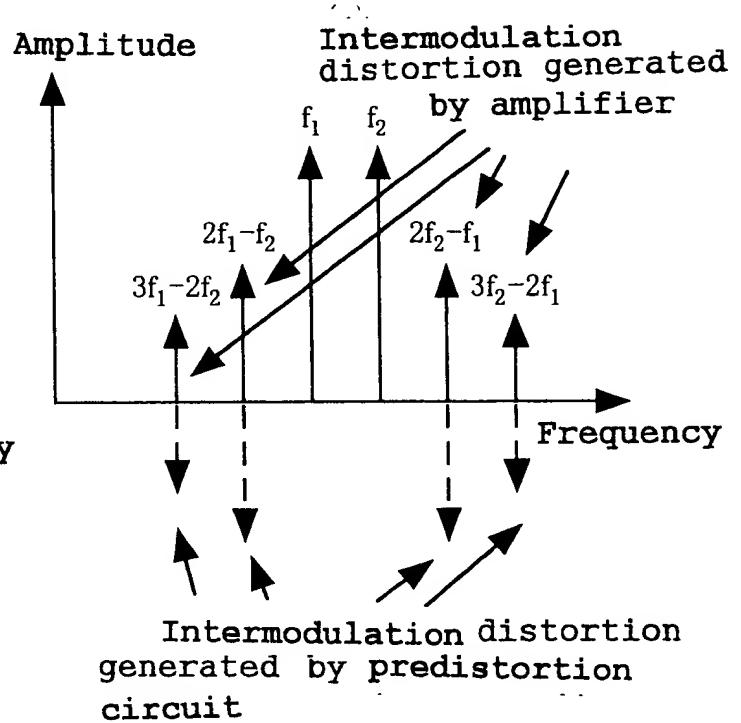


Fig. 6 (a)

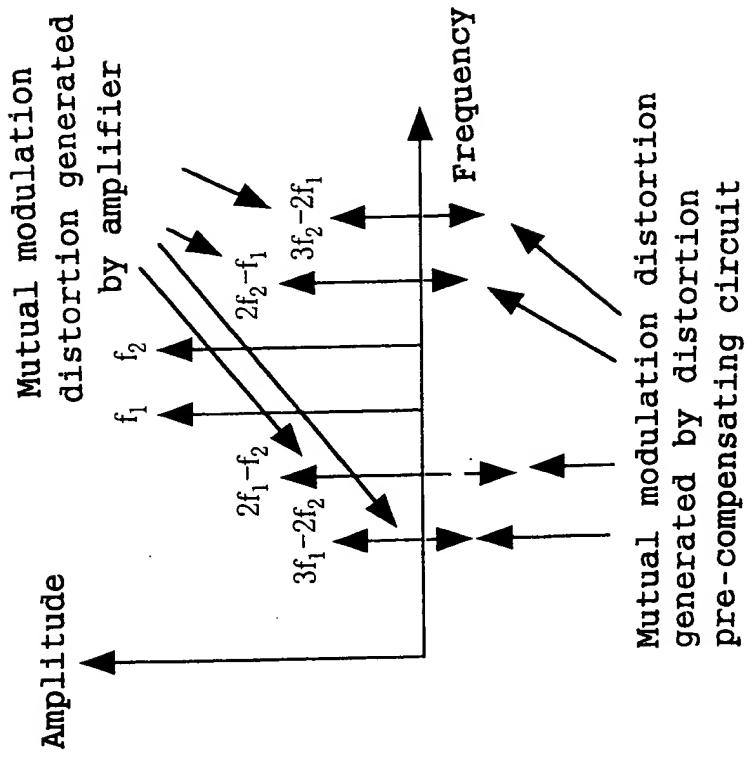
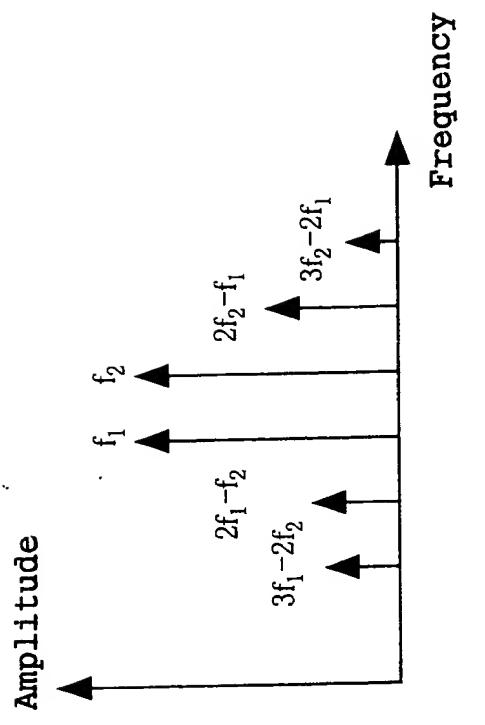


Fig. 6 (b)



Mutual modulation distortion generated by distortion pre-compensating circuit

Fig. 7

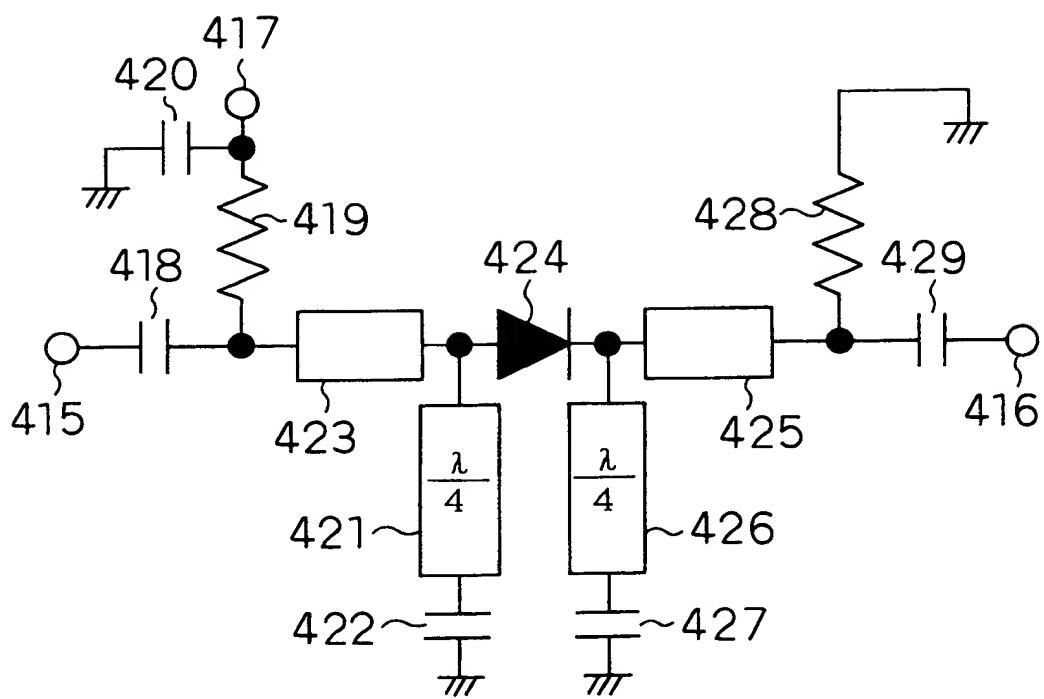


Fig. 8

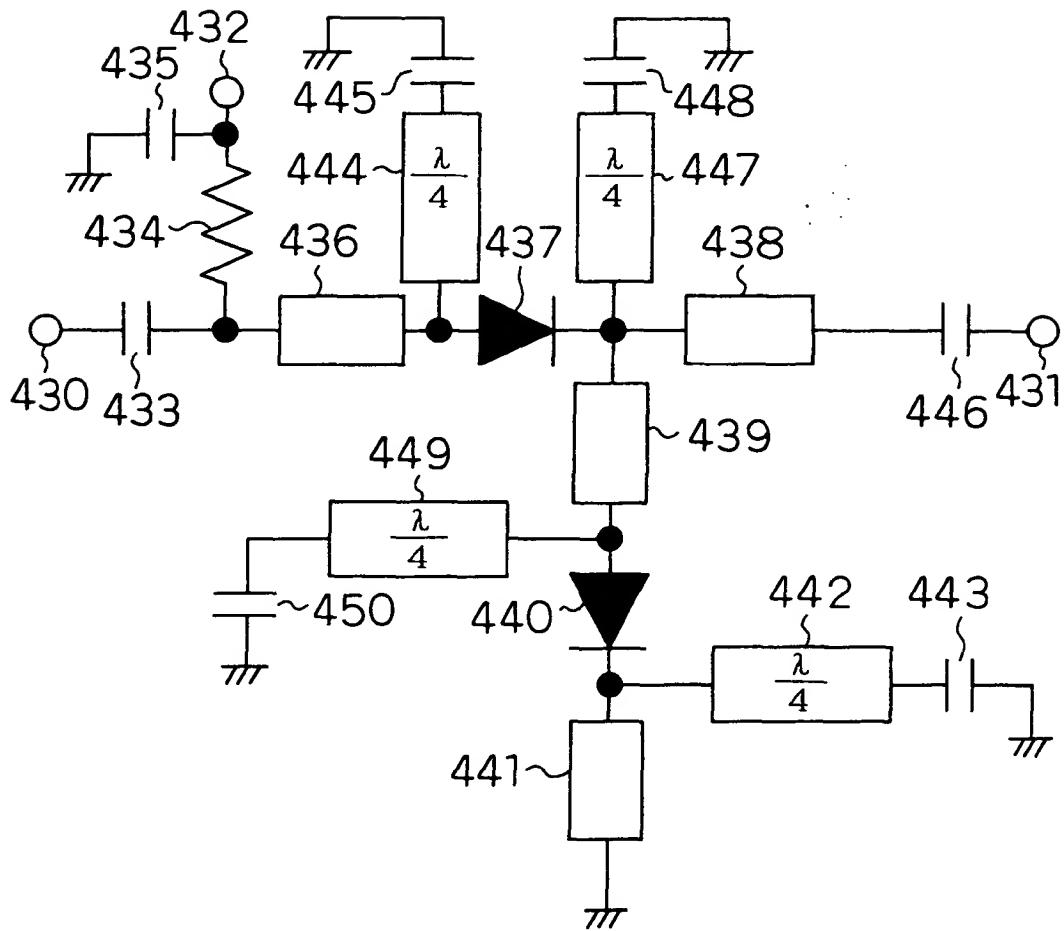


Fig. 9

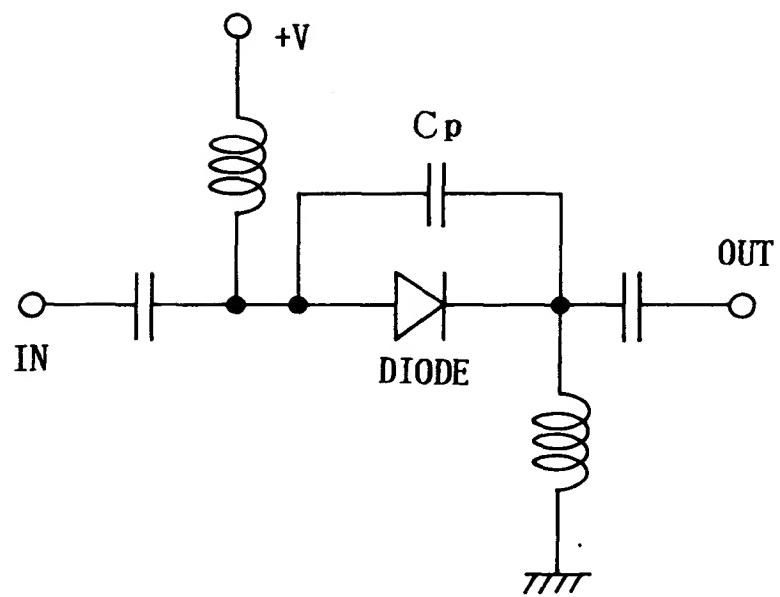


Fig. 10

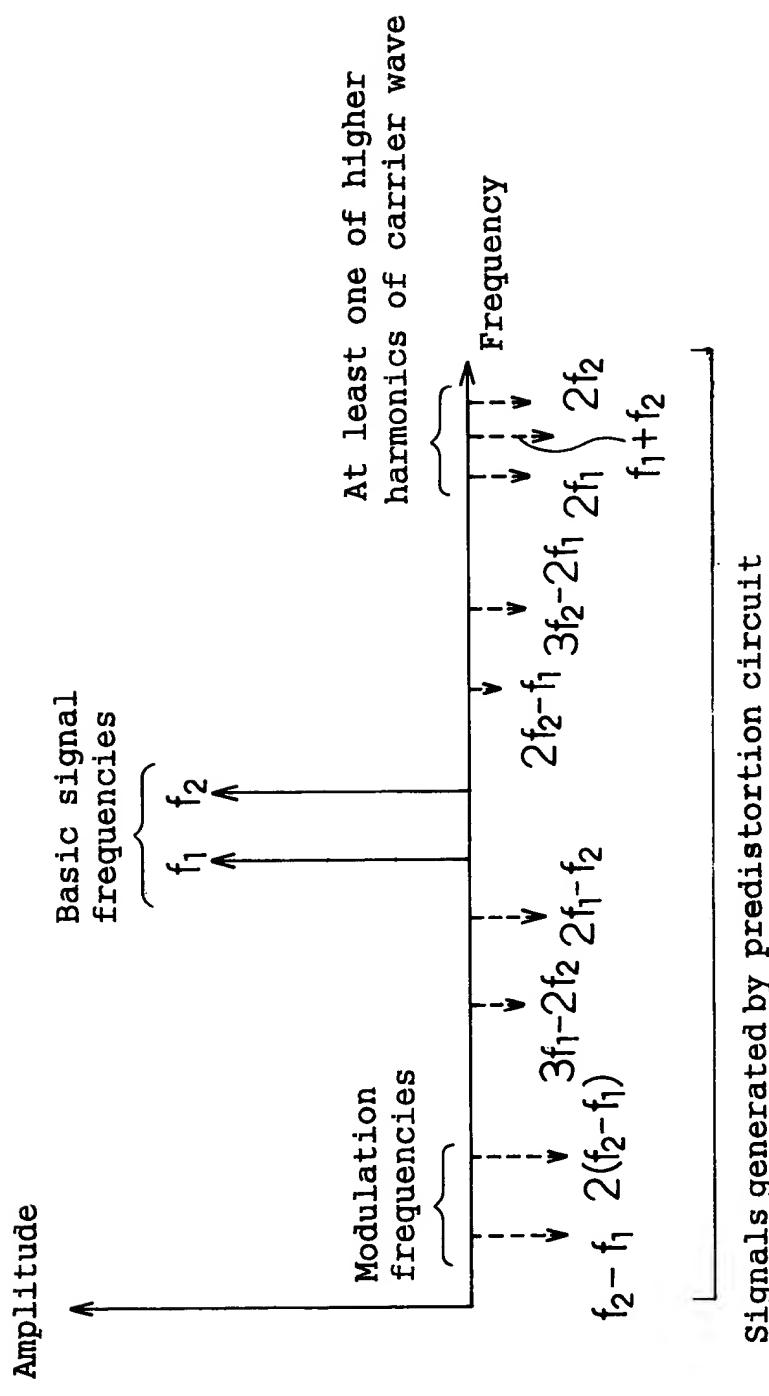


Fig. 11 (a)

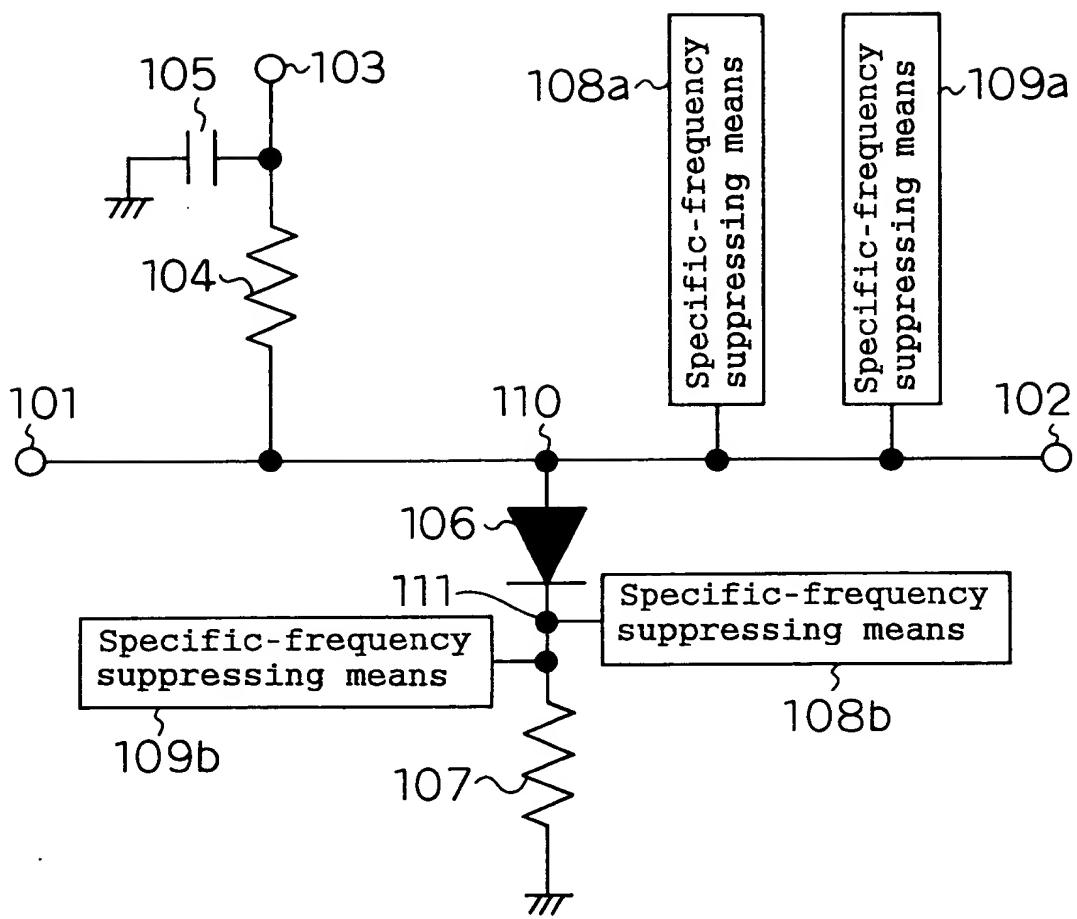


Fig. 11 (b)

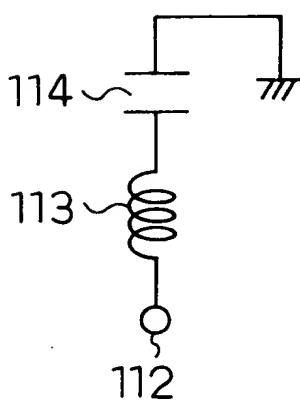


Fig. 11 (c)

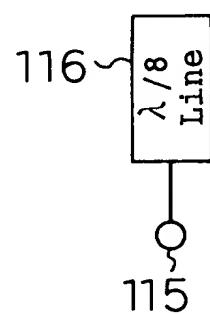
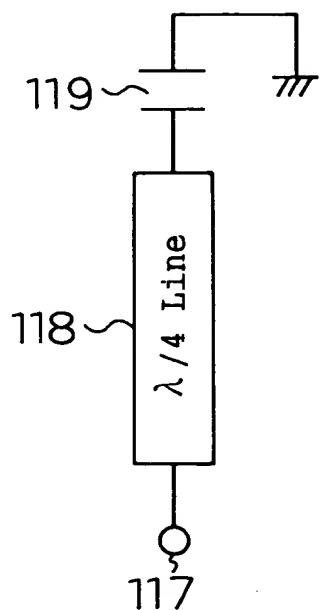


Fig. 11 (d)



புது திட்டம் கொண்டு வருகிறேன்

Fig. 12

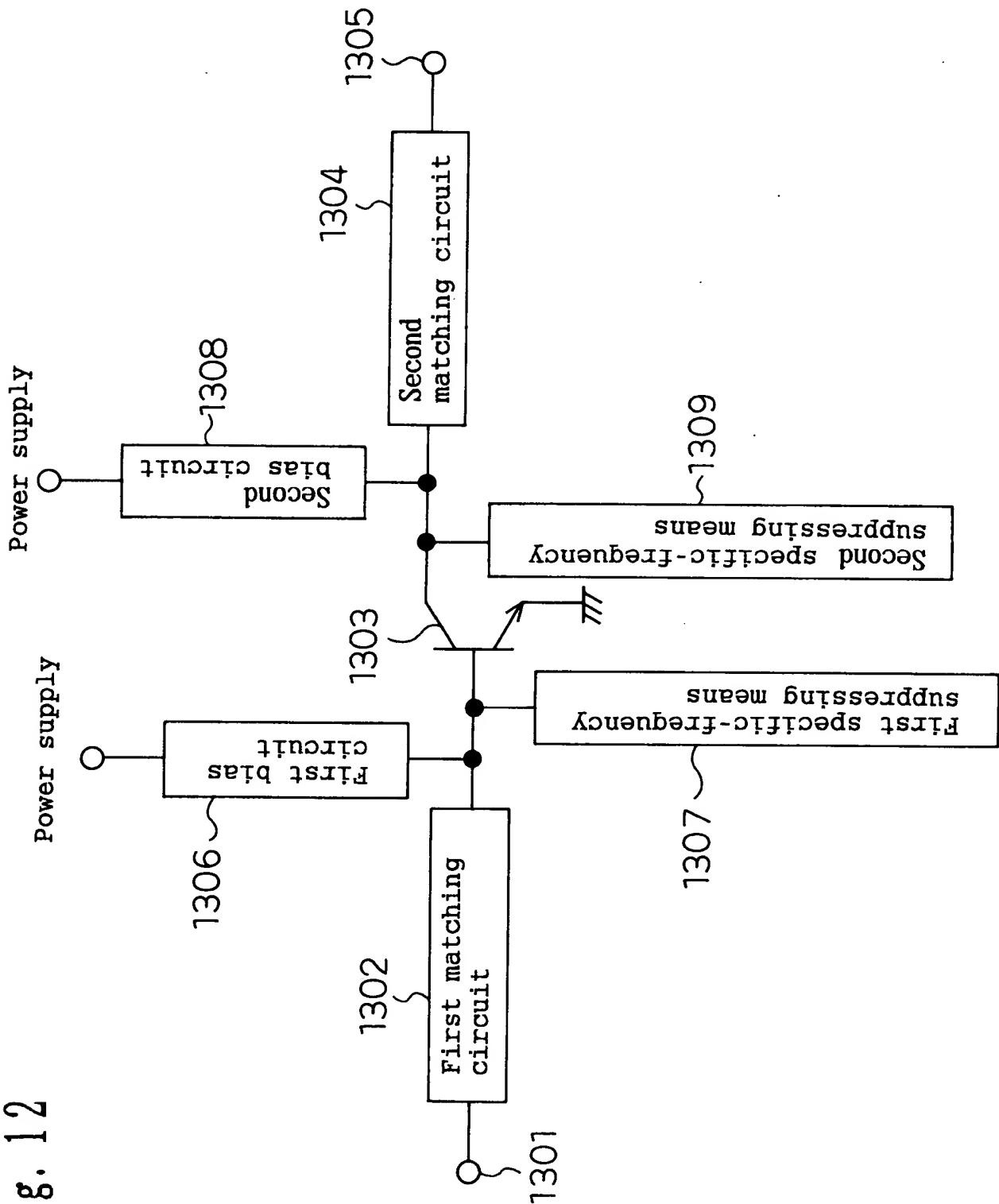
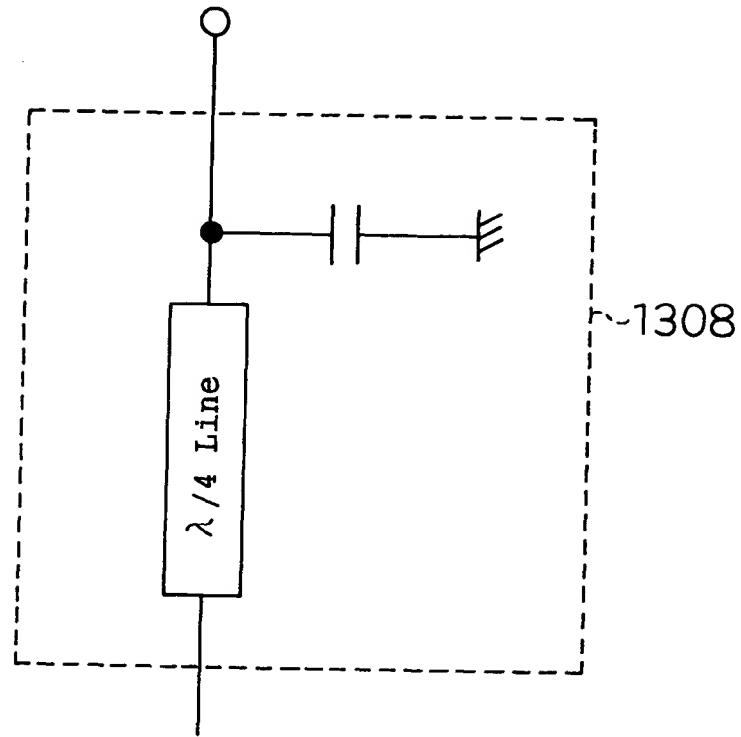


Fig. 13

Power supply



-1308